Computer Organization and Architecture II
CS 3290 3(3-0-3)

Prerequisite: CS2290

Instructor:	Libero Ficocelli
Office:	C424
Phone:	539-2825

Course Content:

This course will introduce students to the fundamentals of logic design and computer architecture. Important topics will include: logic gates, circuit minimization, combinational and sequential logic, latches and flip-flops, memory design (RAM/ROM), PLAs, registers and register transfers, ALU design, control unit design, CPU design, I/O operations and interrupts. The lectures will stress the theoretical aspects of the material while the labs will deal with actual design and physical construction of logic circuits.

Laboratories:

The Laboratory location for this course is in room A201

Textbook:

Theory: **Logic and Computer Design Fundamentals**
M. Morris Mano and Charles R, Kime

Lab: **LogicWorks for Windows: Interactive Circuit Design Software**
Capilano Computing Systems

Grading:

Homework Assignments	
Lab Assignments	
Lab Reports	28%
Class Quizzes	12%
Midterm	25%
Final Exam	35%

Special Notes:

The Student must pass the **theory/concepts** (quizzes, midterm and final exam) portion of the course in order to obtain a passing grade for the term.